

FIG. 1

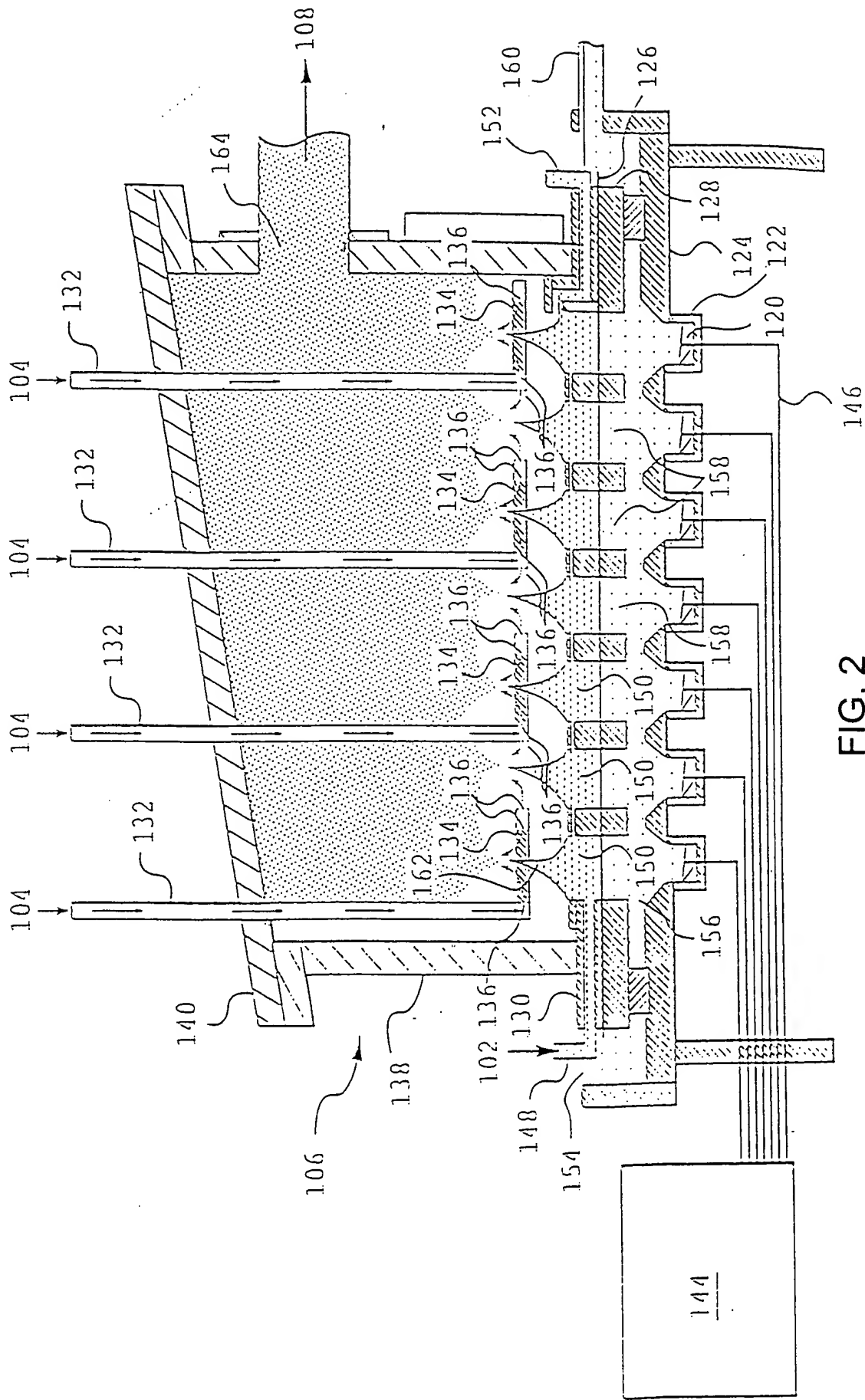


FIG. 2

FOOTED" 888/2660

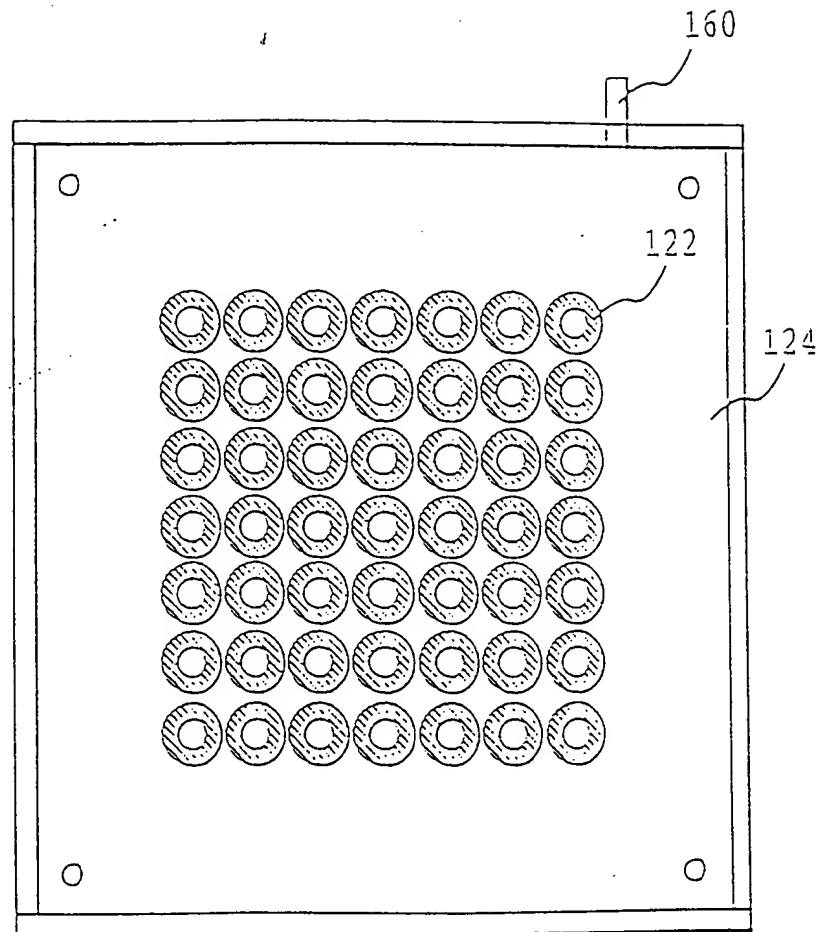


FIG. 3

FOOTED" 88822660

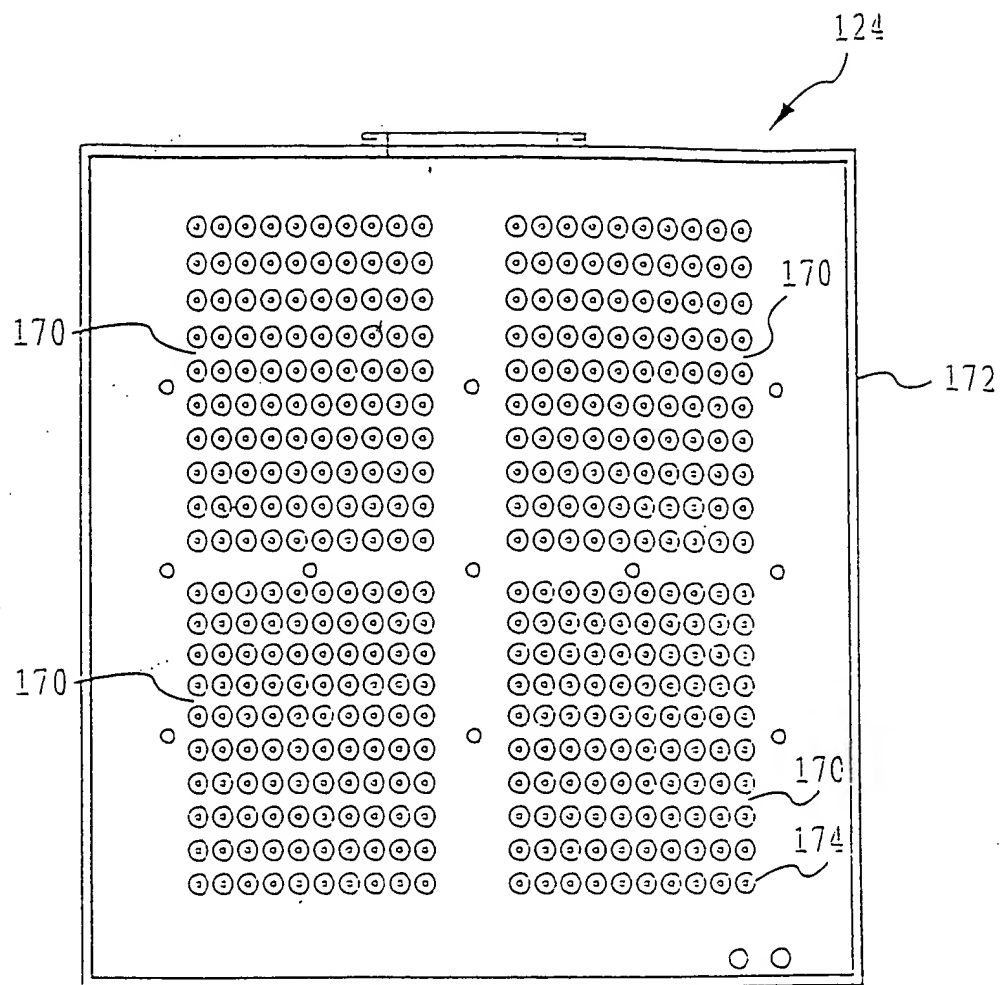


FIG. 4

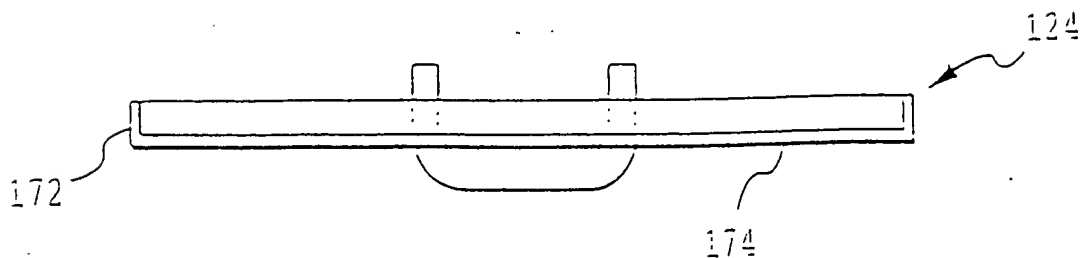


FIG. 5

09927888-081001

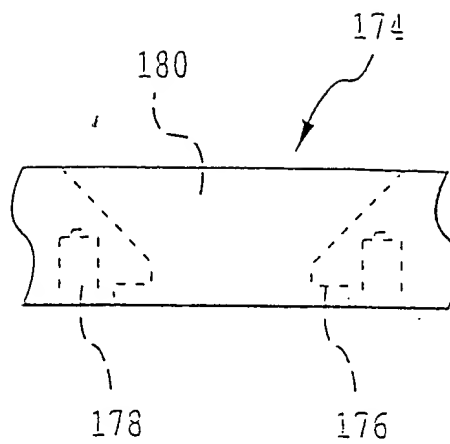


FIG. 6

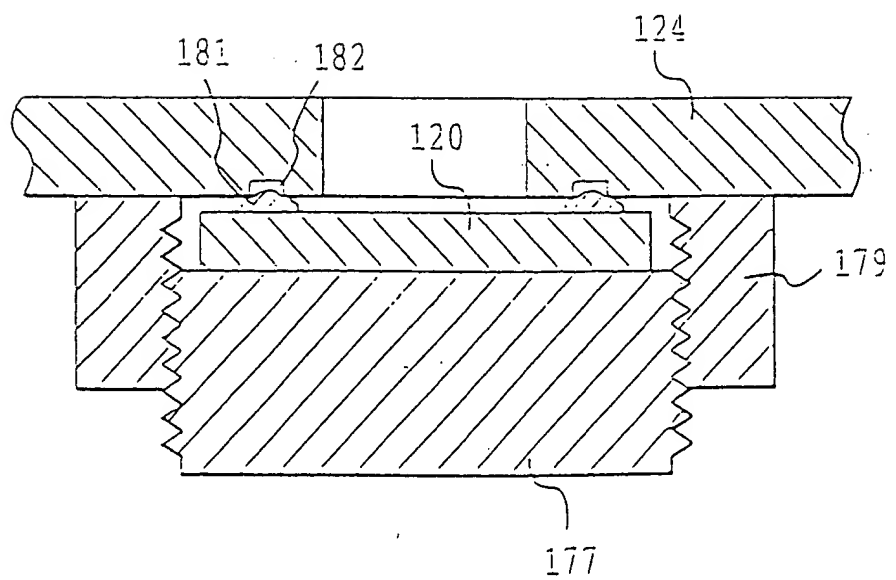


FIG. 7

09927888-031001

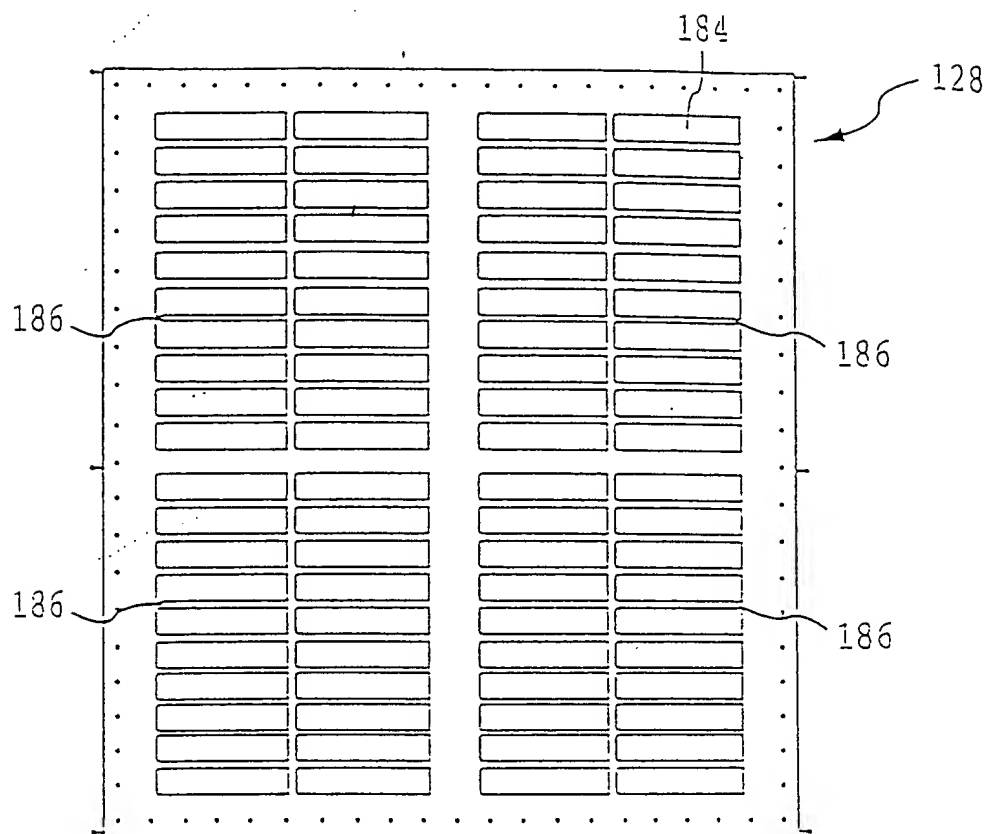


FIG. 8

09927888-081001

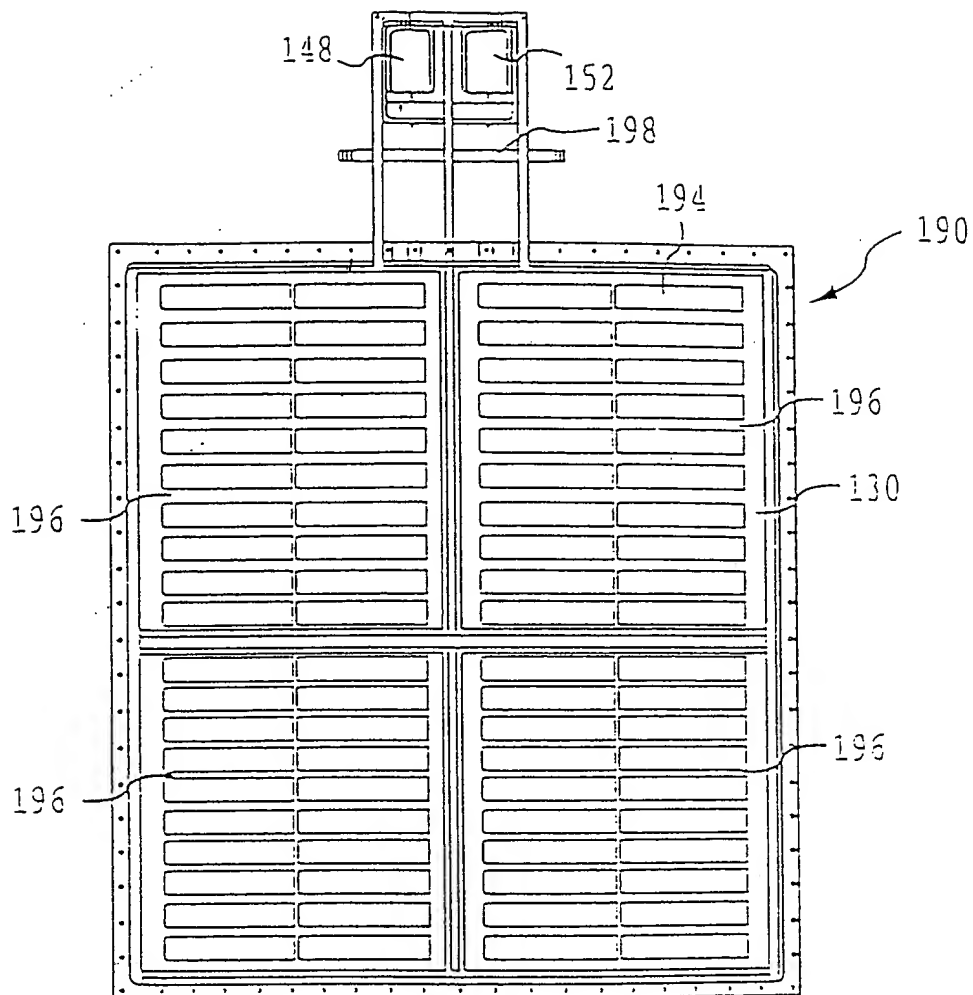


FIG. 9

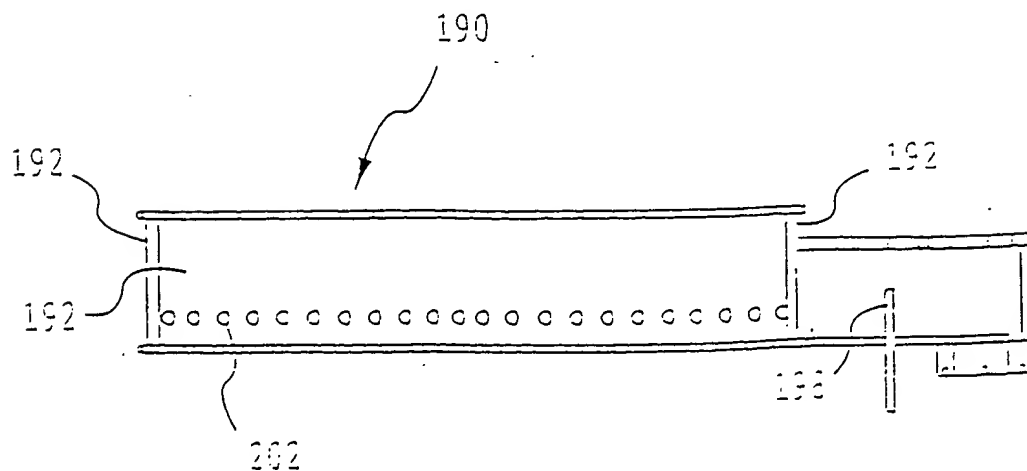


FIG. 10

0927888-081001

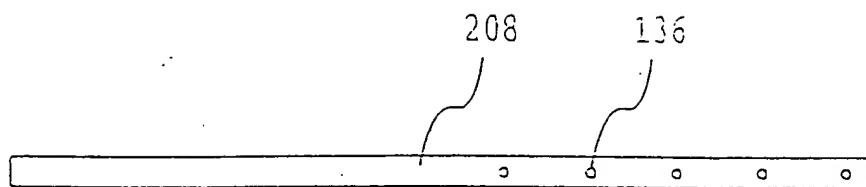


FIG. 11





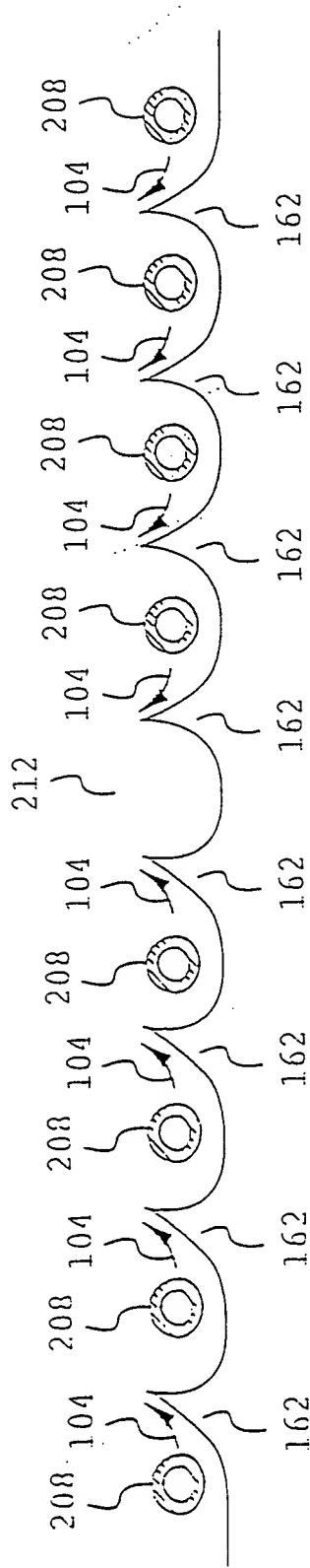


FIG. 13

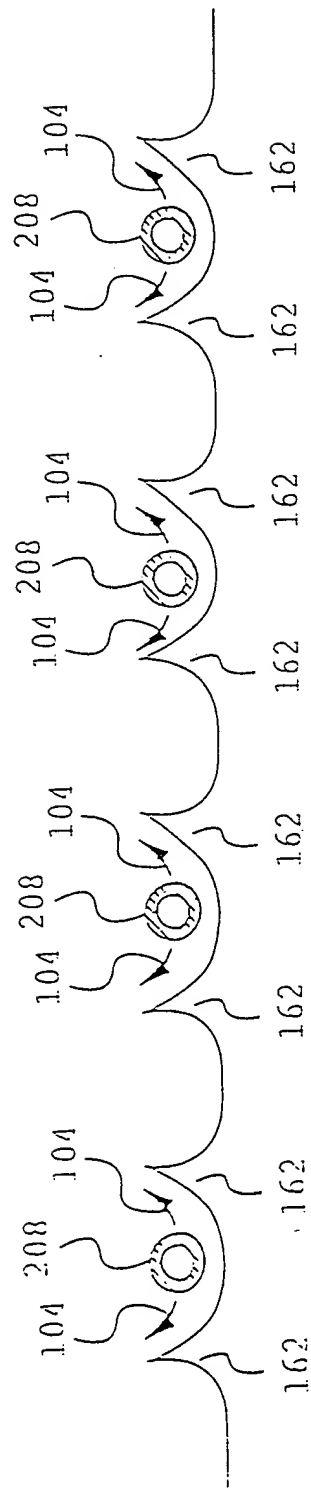


FIG. 14

FIG. 1 is a schematic diagram of a display panel 216. The panel is divided into two sections by a vertical dashed line 208. Each section contains a grid of pixels, with the rightmost column of pixels in each section labeled 218.

FIG. 16

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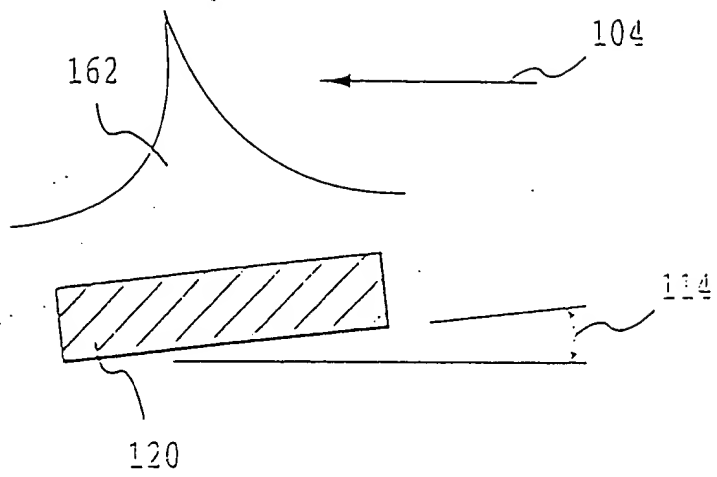


FIG. 17

FIG. 18

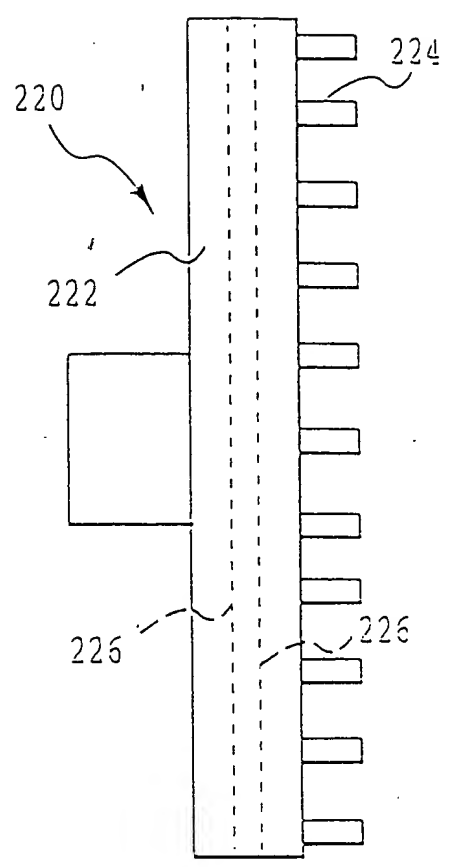


FIG. 18

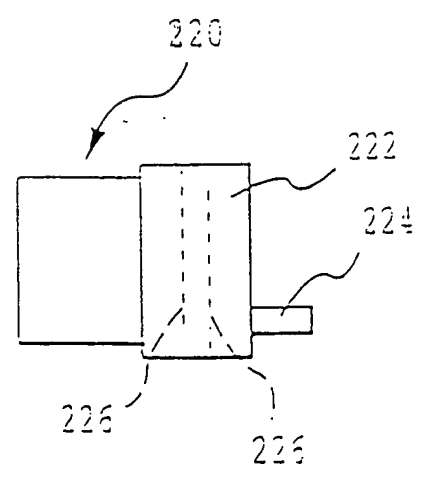


FIG. 19

09927888 001001

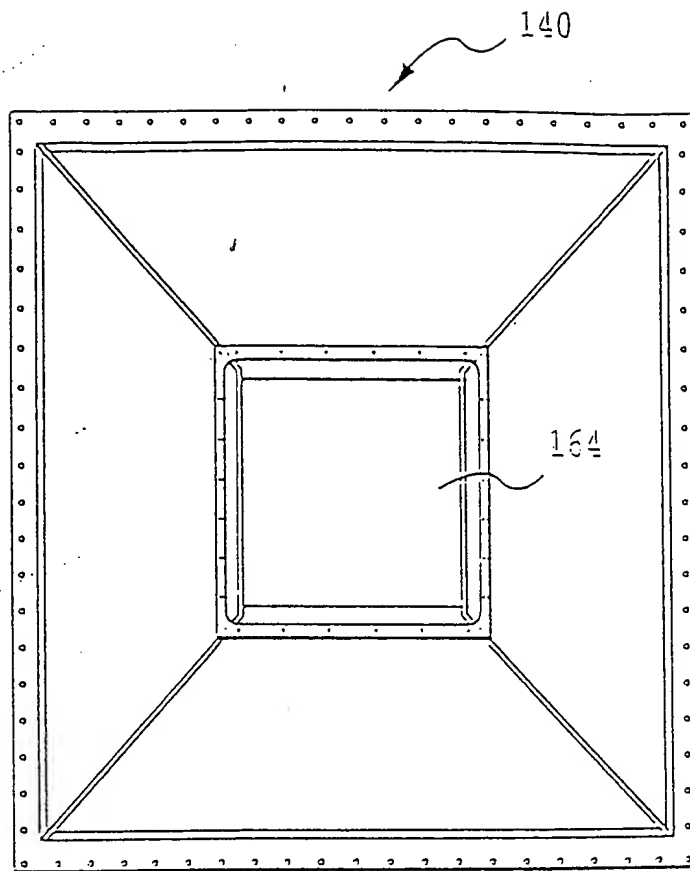


FIG. 20

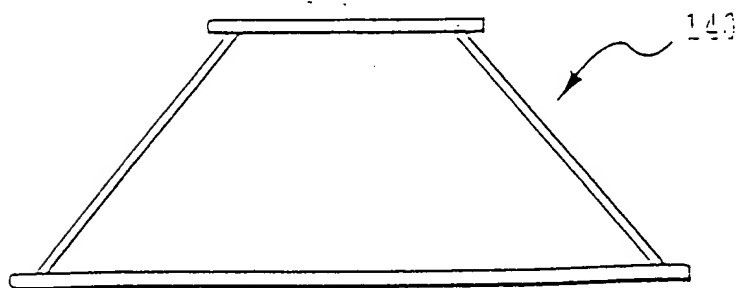


FIG. 21

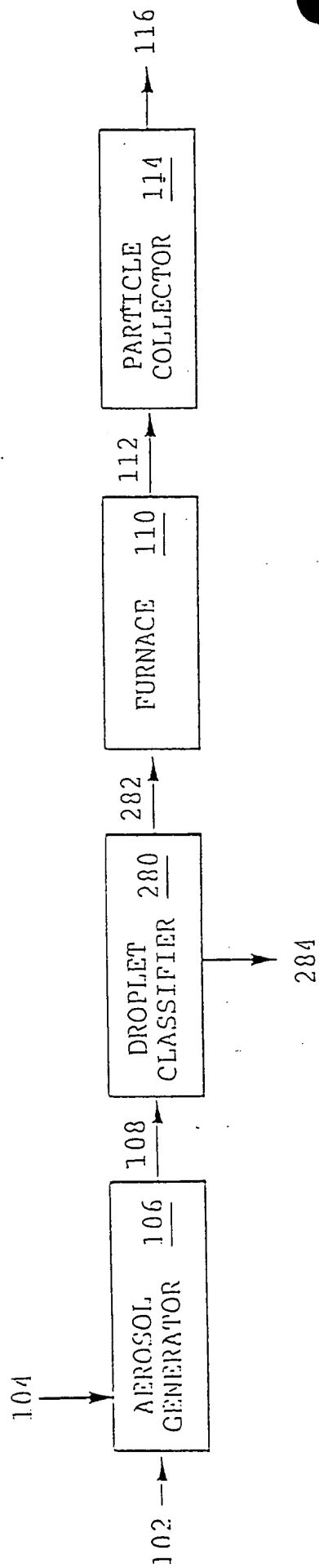


FIG. 22

09022660 88822660

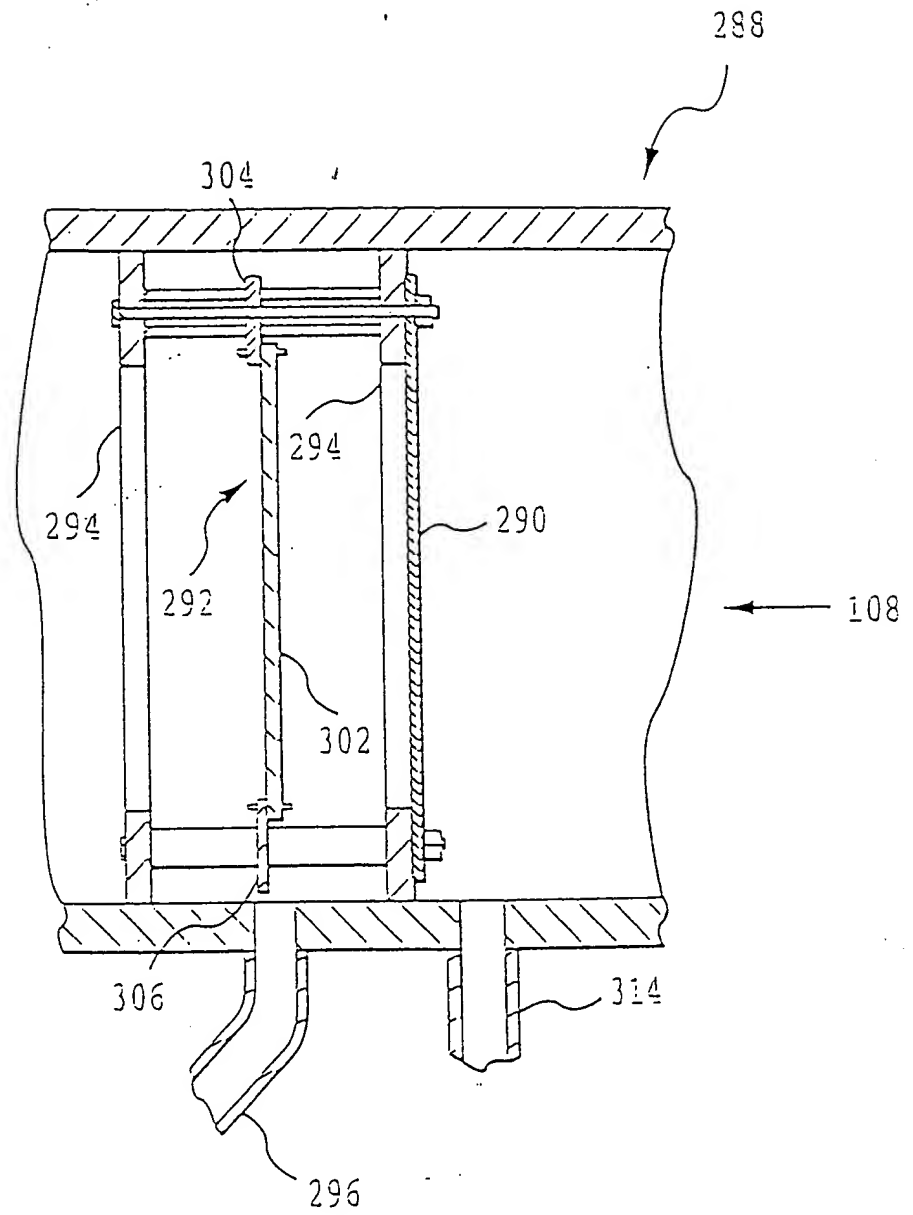


FIG. 23



09927888-081001

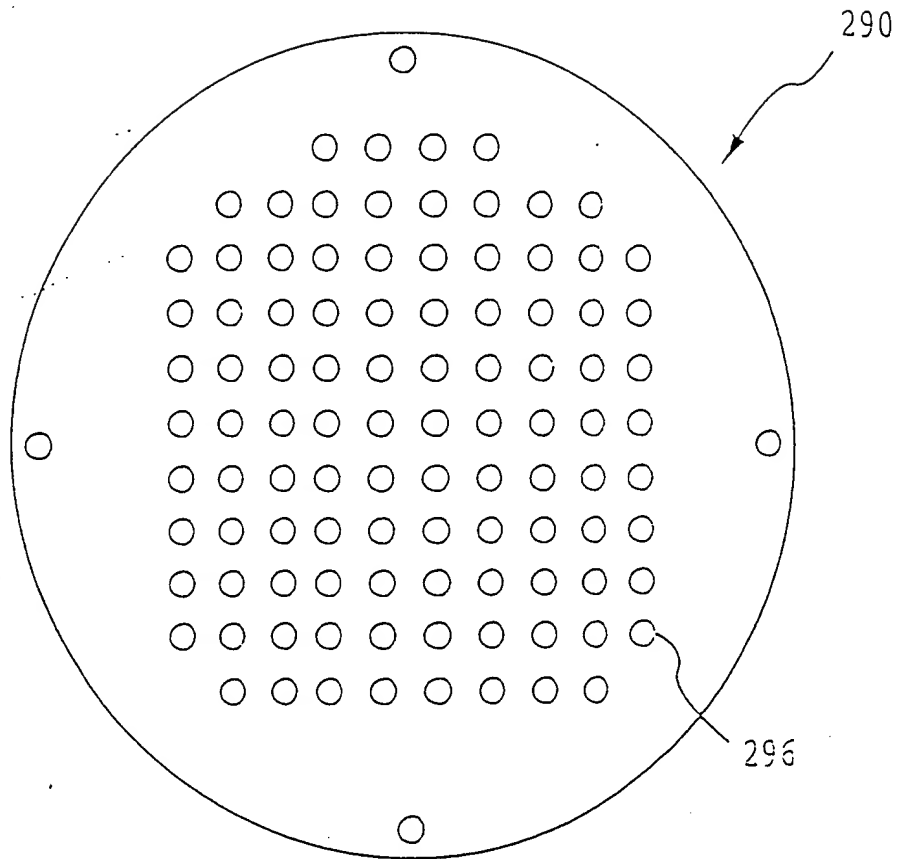


FIG. 24

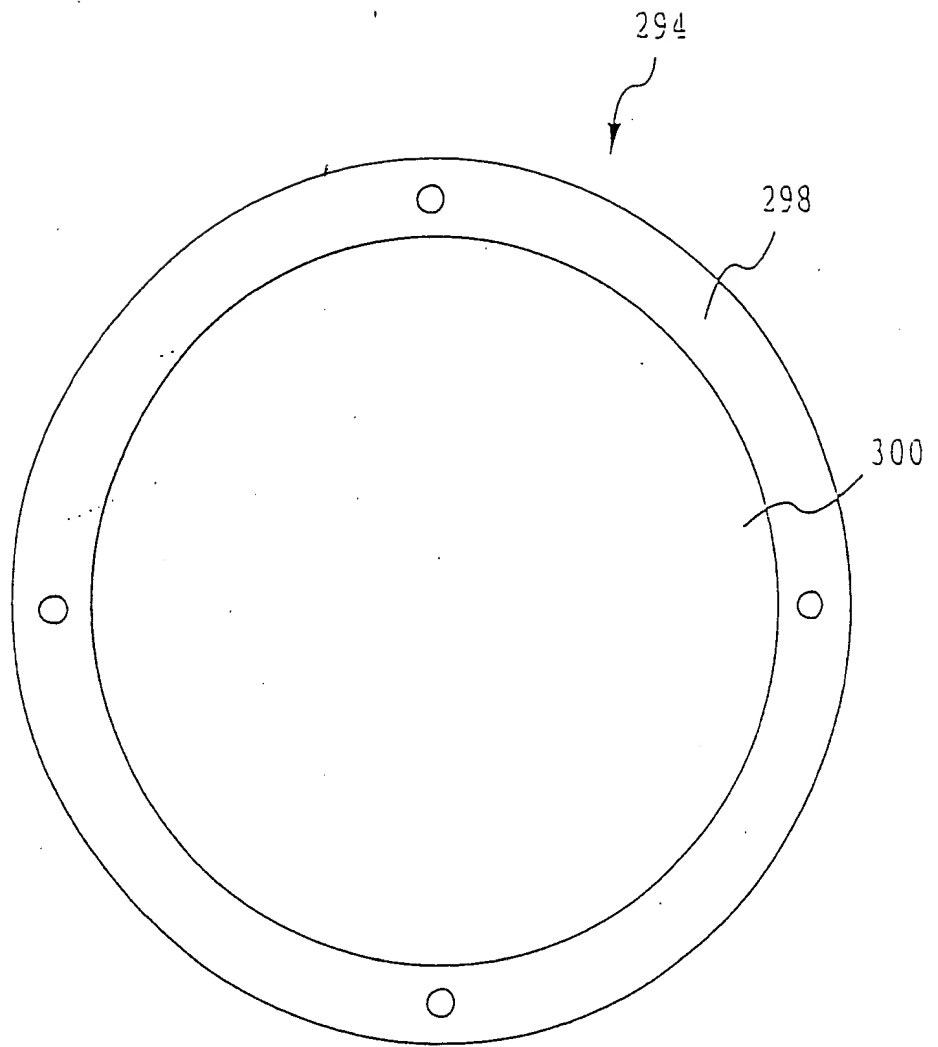


FIG. 25

FIG. 26

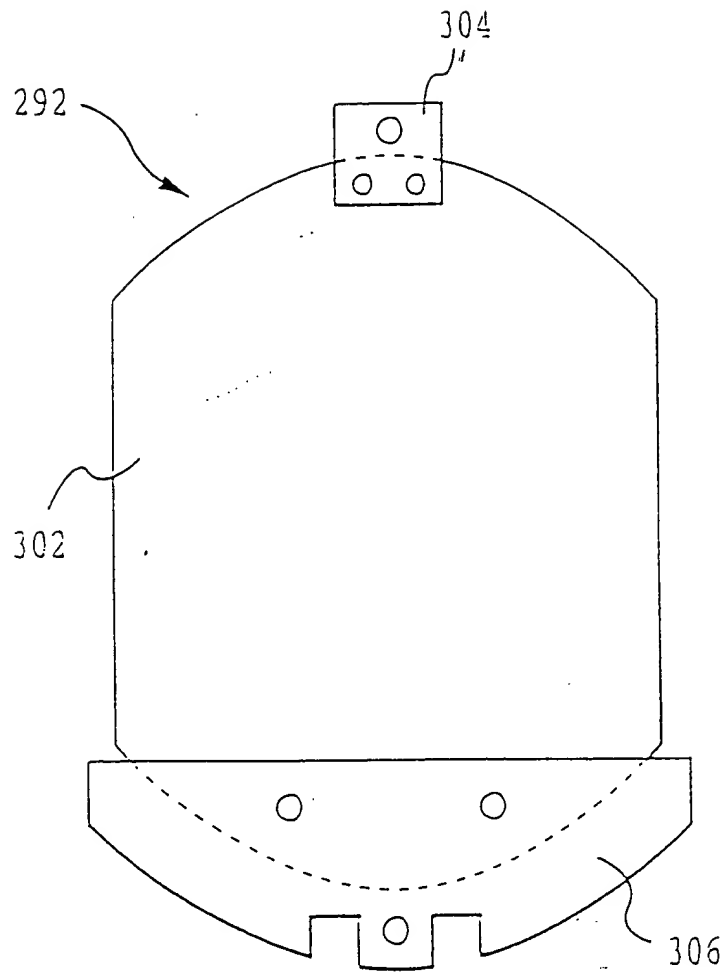


FIG. 26

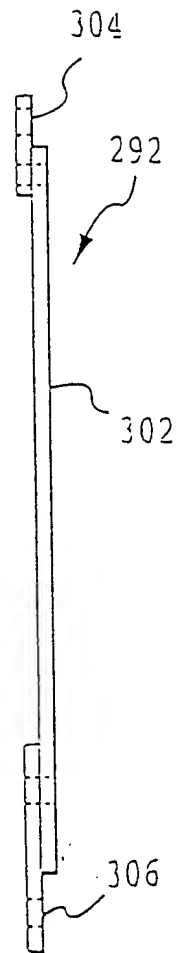


FIG. 27

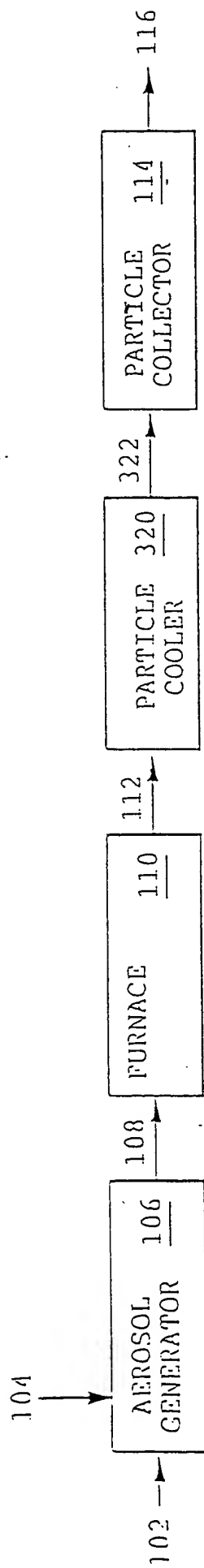


FIG. 28

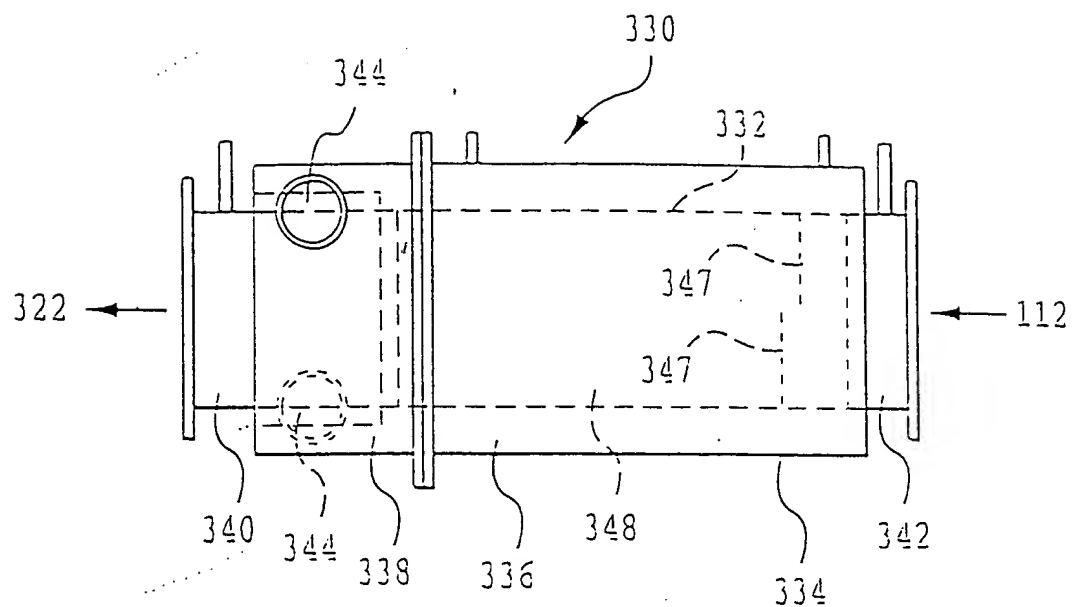


FIG. 29

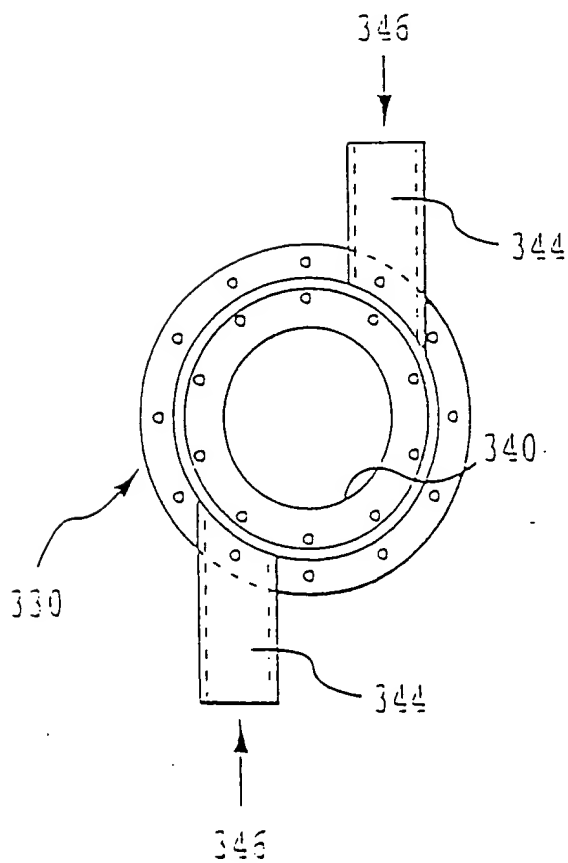


FIG. 30

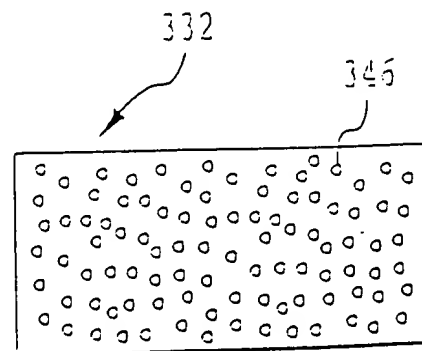


FIG. 31

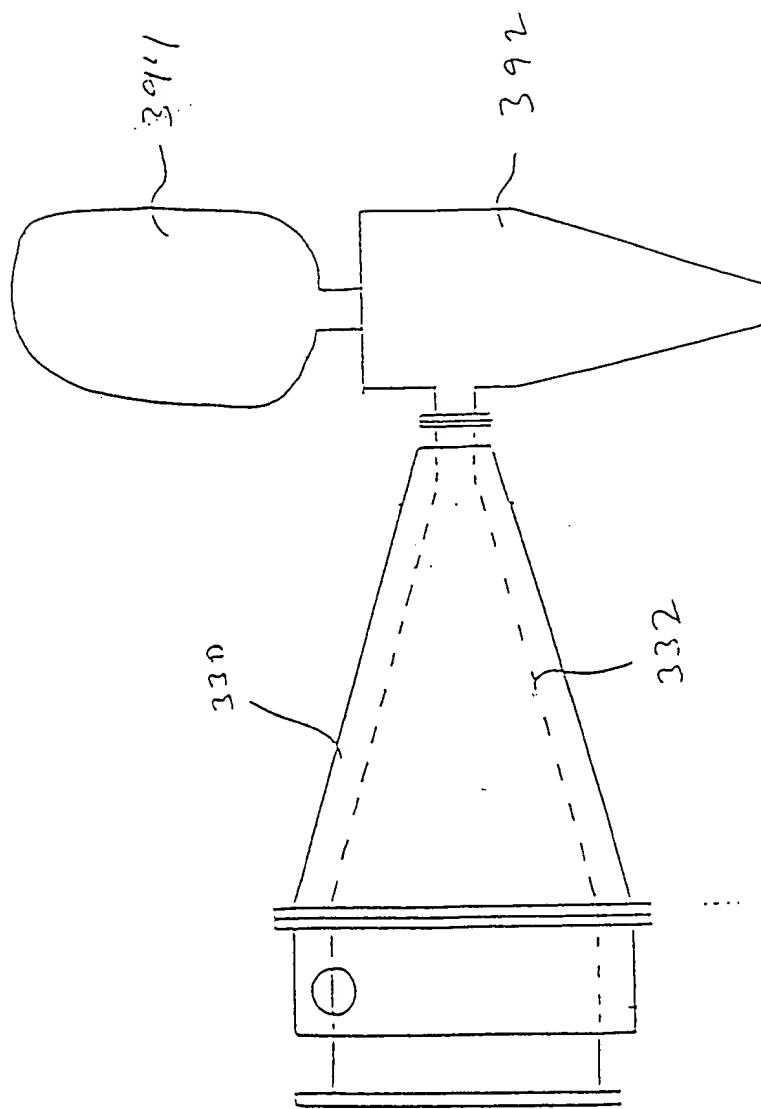


FIG. 32

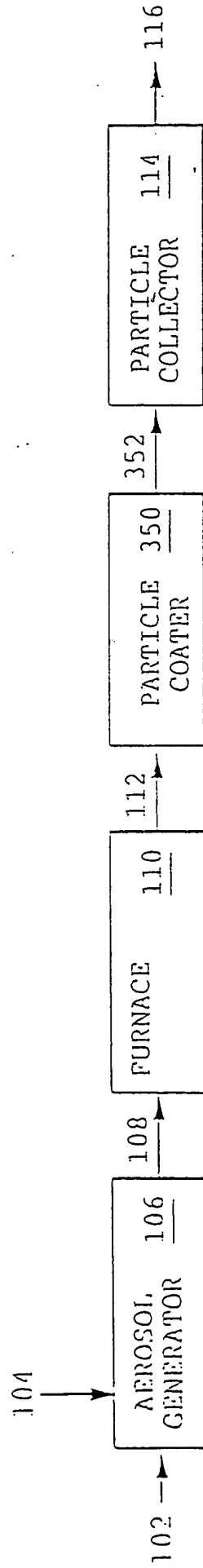


FIG. 33

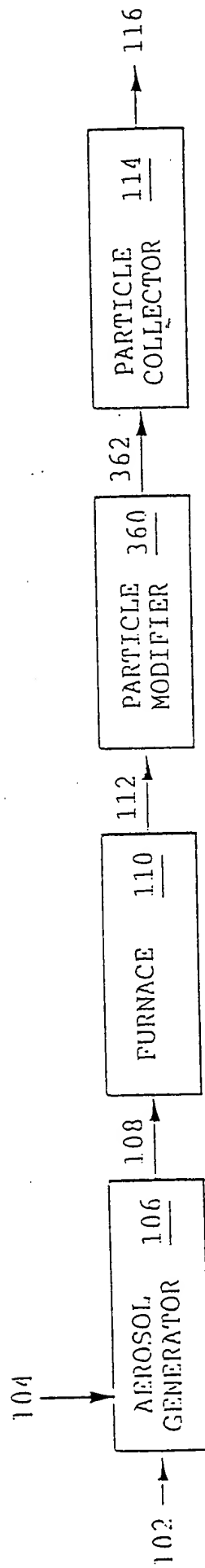


FIG. 34



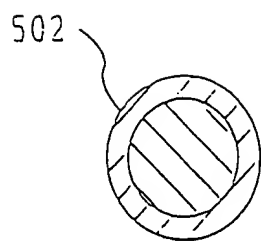


FIG. 35A

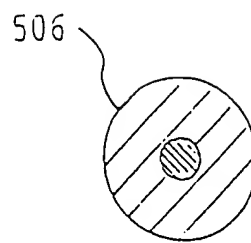


FIG. 35B

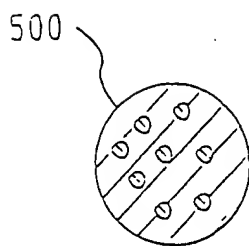


FIG. 35C

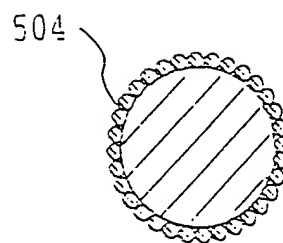


FIG. 35D

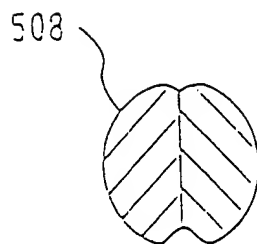


FIG. 35E

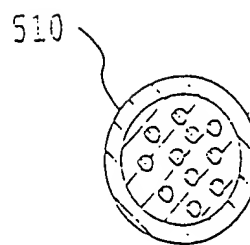


FIG. 35F

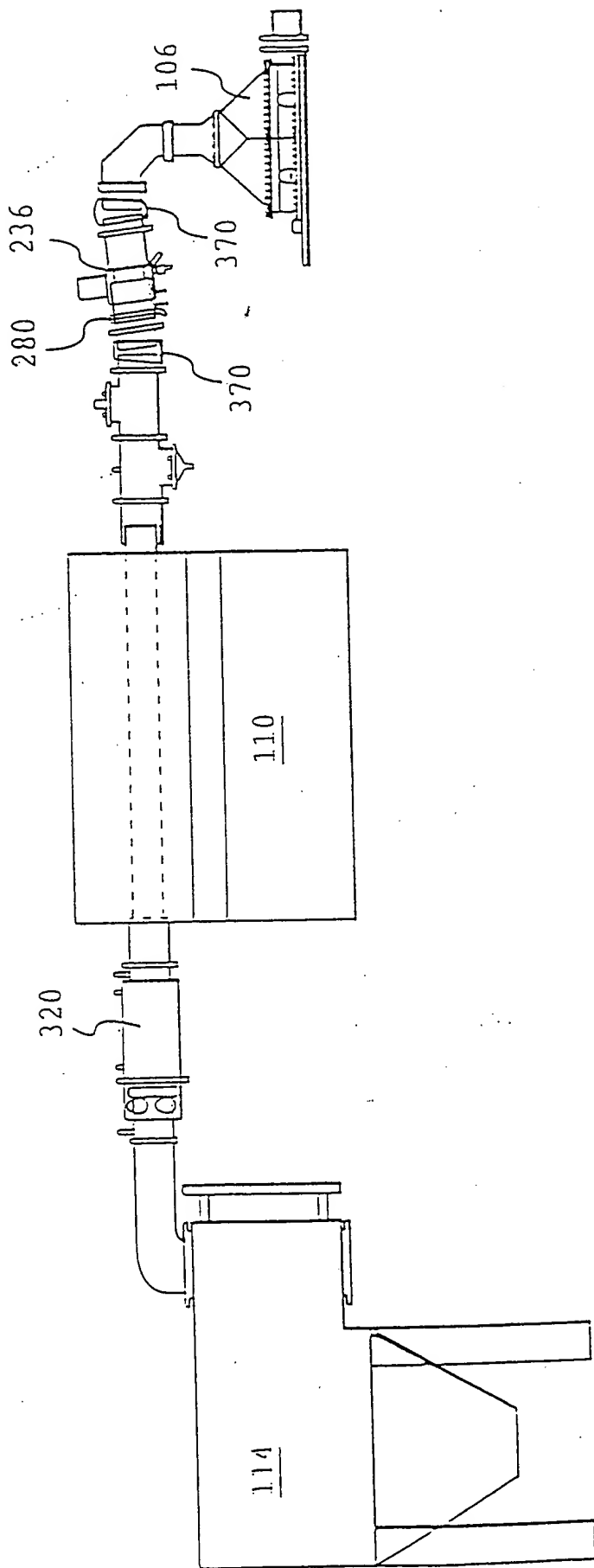


FIG. 36